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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/565,251	01/19/2006	Hiroshi Kobayashi	09792909-6558	8100

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EXAMINER

SHANKAR, VIJAY

ART UNIT PAPER NUMBER

2629

DATE MAILED: 11/03/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/565,251	Applicant(s) KOBAYASHI ET AL.	
	Examiner VIJAY SHANKAR	Art Unit 2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 19 January 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-11 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-3, 5, 7 and 9-11 is/are rejected.
- 7) ☒ Claim(s) 4, 6 and 8 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1-3, 5,7, and 9-11 are rejected under 35 U.S.C. 102(e) as being anticipated by Kashima et al (US 2002/0149558 A1).

Regarding Claims 1 and 9, Kashima et al teaches a display apparatus comprising: a pixel array unit including pixels arranged in a matrix and signal lines arranged so that one corresponds to each vertical column of pixels in the matrix of pixels (Fig.11, Paragraph 0046-0051); clock generating means (18 in Fig.11) for generating a horizontal start pulse for indicating a start of horizontal scanning, first clock pulses being used as a basis for the horizontal scanning, and second clock pulses having n phases and being synchronized with the first clock pulses, where n is an integer equal to or greater than three (Fig.11-17, Paragraph 0046-0069); a shift register

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(21 in fig.11) including cascaded shift register stages for sequentially transferring the horizontal start pulse in synchronism with the first clock pulses, the shift register being configured to successively output transfer pulses from the shift register stages (Fig.11-17, Paragraph 0046-0069); a group of first switches (23-1 in Fig.11) configured to successively generate sampling pulses by extracting the second clock pulses in response to the transfer pulses successively output from the shift register (Fig.11-17, Paragraph 0024-0025, 0046-0071); and a group of second switches (23-2 in fig.11) configured to successively sample input video signals in response to the sampling pulses generated by the group of first switches and to provide the video signals to the signal lines in the pixel array unit (Fig.11-17, Paragraph 0024-0025, 0046-0069), wherein the start pulse has a pulse width that includes a plurality of pulse widths of the first clock pulses (Paragraph 0010-0013, 0024-0025, 0052-0056).

Regarding Claims 2,5,10, Kashima et al teaches a display apparatus wherein a pulse interval of the second clock pulses is n times a pulse interval of the first clock pulses, where n is an integer equal to or larger than three. (Paragraph 0010-0013, 0024-0025, 0052-0056).

Regarding Claims 3,6,11, Kashima et al teaches a display apparatus wherein a pulse width of each of the second clock pulses is larger than a pulse width of each of the first clock pulses. (Paragraph 0010-0013, 0024-0025, 0052-0056).

Regarding Claim 7, Kashima et al teaches a display apparatus wherein electrooptic elements in the pixels are liquid crystal cells. (LC in Fig.11; Paragraph 0046-0048).

Allowable Subject Matter

4. Claims 4,6,8 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

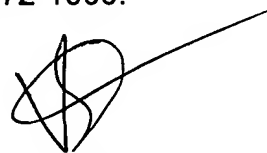
5. The following is an examiner's statement of reasons for allowance: The prior arts fails to teach the display apparatus wherein the group of second switches are divided into at least two subgroups including a first subgroup of nonadjacent second switches and a second subgroup of second switches arranged next to the second switches in the first subgroup, the video signals are divided into at least two systems and then provided to the at least two subgroups of second switches, and the sampling pulses generated by the group of first switches are divided and provided to the at least two subgroups of switches in the group of second switches as claimed in Claims 4,6,8.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VIJAY SHANKAR whose telephone number is (571) 272-7682. The examiner can normally be reached on M-F 7:00 am - 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, BIPIN SHALWALA can be reached on (571) 272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



VIJAY SHANKAR
Primary Examiner
Art Unit 2629

VS